



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Bulent DERVISOGLU et al.

Appl. No: 10/750,949

Filed: January 5, 2004

For: ACCELERATED SCAN CIRCUITRY
AND METHOD FOR REDUCING
SCAN TEST DATA VOLUME
AND EXECUTION TIME

Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

Atty. Docket No: 41567-199588

Customer No:

26694

PATENT TRADEMARK OFFICE

Information Disclosure Statement

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is an Information Disclosure Statement submitted under 37 C.F.R. § 1.97 within the time specified under 37 C.F.R. § 1.97(b).

In order to comply with applicant's duty of disclosure under 37 C.F.R. § 1.56, the U.S. Patent and Trademark Office is notified of the documents which are listed on the attached Form PTO-1449 and which the Examiner may deem relevant to patentability of the claims of the above-identified application. No copies of the documents are being provided with this Information Disclosure Statement, as all of the cited documents were cited in connection with a parent application, Serial No. 10/351,276 (currently pending).

The present Information Disclosure Statement is being filed before the mailing date of the first Office Action on the merits, and therefore no Statement Under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

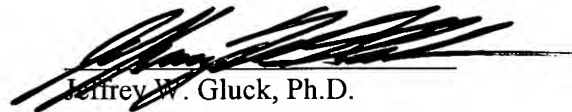
In view of the above, no further translation or statement of relevance is required, and as all requirements of 37 C.F.R. § 1.97 and all official guidelines pertaining to Information

Disclosure Statements have been complied with, and it is therefore respectfully requested that the Examiner consider the documents and make them of record.

Please charge any necessary fee or credit any overpayment in connection with this Information Disclosure Statement to Deposit Account No. 22-0261.

Respectfully submitted,

Date: April 1, 2004



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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031
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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 2 of 2

Complete if Known

| | |
|------------------------|-------------------|
| Application Number | 10/750,949 |
| Filing Date | January 5, 2004 |
| First Named Inventor | Bulent DERVISOGLU |
| Group Art Unit | Not Yet Assigned |
| Examiner Name | Not Yet Assigned |
| Attorney Docket Number | 41567-199588 |

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials * | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
|------------------------|--------------------------|---|----------------|
| | A9 | V. AGRAWAL et al., "A Tutorial on Built-In Self-Test, Part 1: Principles," IEEE Design & Test of Computers, pp. 73-82, March 1993. | |
| | A10 | V. AGRAWAL et al., "A Tutorial on Built-In Self-Test, Part 2: Applications," IEEE Design & Test of Computers, pp. 69-77, June 1993. | |
| | A11 | C. BARNHART et al., "OPMISR: The Foundation for Compressed ATPG Vectors," IEEE ITC International Test Conference, Paper 27.4, pp. 748-757, Oct. 30-Nov. 1, 2001. | |
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Examiner
Signature

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Considered

¹ EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

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